

Appl. No. 10/677,929
Amdt. dated 12/29/2004
Reply to Office Action of 9/29/2004

Attorney Docket No.: N1085-00154
TSMC 2003-285

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: **Cheng I. Sun**

Examiner: **Alexander J.
Kosowski**

Serial No.: **10/677,929**

Group Art Unit: **2125**

Filed: **10/02/2003**

Confirmation No.: **9602**

For: METHOD AND SYSTEM FOR ANALYZING SEMICONDUCTOR FABRICATION

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Date: December 29, 2004

Maria E. Provencio

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Examiner Alexander J. Kosowski
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RESPONSE TO OFFICE ACTION DATED SEPTEMBER 29, 2004

Sir:

Responsive to the Office Action dated September 29, 2004, please re-examine and reconsider the above-identified patent application according to the amendments and remarks provided.

Amendments to the Specification are shown on page 2 of this paper.

Amendments to the Claims are reflected in the Listing of Claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 9 of this paper.